

MODEL 580



STRATUM 3 PERFORMANCE

TEMPERATURE COMPENSATED CRYSTAL OSCILLATOR

FEATURES

- Clipped Sine Output
- Optional Voltage Control for Frequency Tuning [VCTCXO]
- 5.0mm x 3.2mm Surface Mount Package
- Frequency Range 5 52 MHz
- Fundamental Crystal Design
- Operating Voltage, +3.3Vdc or +5.0Vdc
- Overall Frequency Stability ±4.6ppm
- Operating Temperature to -40°C to +85°C
- Tape & Reel Packaging Standard, EIA-418
- RoHS/Green Compliant [6/6]



APPLICATIONS

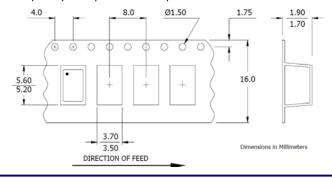
The Model 580 is a quartz based analog TCXO with a Clipped Sine output and optional frequency tuning. M580 is suitable for applications requiring Stratum 3 performance such as base stations, small cells, 1588 and Synchronous Ethernet timing, wireless communications, test and measurement.

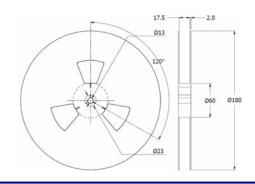
ORDERING INFORMATION 580 PACKAGING OPTIONS **SUPPLY VOLTAGE** R = +3.0VdcS = +5.0VdcT - 1k pcs./reel L = +3.3VdcFREQUENCY FREQUENCY TUNING [AFC] T = No AFC [TCXO] Product Frequency Code [3 digits] Refer to document 016-1454-0, Frequency $A = \pm 5ppm - \pm 8ppm [VCTCXO]$ Code Tables. **OPERATING TEMPERATURE RANGE** FREQUENCY STABILITY * $W = 0^{\circ}C \text{ to } +55^{\circ}C$ H = -10°C to +60°C $X5 = \pm 0.05$ ppm ¹ $X2 = \pm 0.28ppm$ C = -20°C to +70°C $01 = \pm 0.10$ ppm² $05 = \pm 0.50$ ppm $D = -30^{\circ}C \text{ to } +85^{\circ}C$ $02 = \pm 0.20$ ppm I = -40°C to +85°C * Frequency vs. Temperature Only 1] Only available with temperature range codes "W" and "H". 2] Only available with temperature range codes "W", "H" and "C". Not all performance combinations and frequencies may be available.

Contact your local CTS Representative or CTS Customer Service for availability.

PACKAGING INFORMATION [reference]

Device quantity is 1k pcs. maximum per 180mm reel.



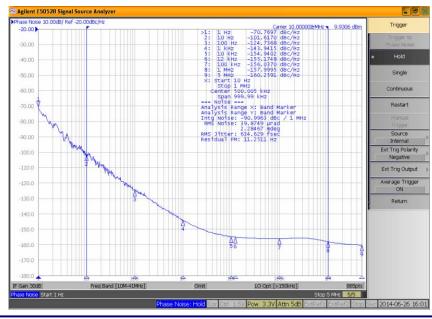


ELECTRICAL CHARACTERISTICS

Ma Sto	aximum Supply Voltage aximum Control Voltage corage Temperature	V _{CC} V _C	<u>-</u>	-0.6	-	6.0	V
Sto	orage Temperature		_				
	· ·	т		-0.5	-	V_{CC}	V
Ор		T_{STG}	-	-40	-	+100	°C
	perating Temperature				+25		
О	Order Code 'C'	T_A	-	-20		+70	°C
С	Order Code 'I'			-40		+85	
Fre	requency Range	f_0	-	5	-	52	MHz
Su	upply Voltage						
О	Order Code 'R'	V _{cc}	±5%	2.85	3.0	3.15	v
С	Order Code 'L'		±3%	3.14	3.3	3.47	v
% 0	Order Code 'S'			4.75	5.0	5.25	
ELECTRICAL PARAMETERS	upply Current	I_{CC}		-	-	3.5	mA
Fre	requency Stability						
S O	Overall Frequency Stability	Δf/f _O	Reference to f _O , Including 20 years aging	-	-	4.60	
<u>a</u> v:	vs. Initial Calibration	Δι/ι ₀	@ +25°C, at time of shipment	-	-	1.00	
V SPI	vs. Operating Temperature ¹		[Fmax Fmin.]/2, over -40°C to +85°C	-	-	0.28	
\ \frac{\frac{1}{2}}{2} \text{v:}	vs. Supply Voltage	∆f/f ₂₅	±5% change @ +25°C	-	-	0.20	± ppm
Si v:	vs. Load	Δі/1 ₂₅	±5% change	-	-	0.20	
H .	vs. Aging		20 years @ +40°C	-	-	3.00	
Но	oldover	Δf/f _O	[Fmax Fmin.]/2, over 24 hours	-	-	0.40	
Co	ontrol Voltage	V_{C}	-	0.5	1.5	2.5	V
Fre	requency Tuning [VCTCXO Only]	-	$V_C = 1.5V \pm 1.0V$, monotonic positive	5 - 8			± ppm
V_{C}	Input Impedance	ZV_C	-	100	-	-	kOhm
Ou	utput Waveform		AC coupled Clipped Sinewave				
Ou	utput Voltage Levels			0.8	-	-	Vp-p
Ou	utput Load	$R_L // C_L$	-	10k0	Ohm // :	10pF	
Ou	utput Duty Cycle	SYM	@ 50% Level	45	-	55	%
Sta	art Up Time	T_S	-	-	-	2	ms
Ph	nase Noise ²	-	-				dBc/Hz

Notes:

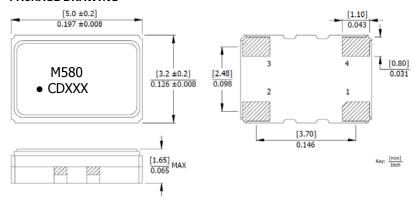
- 1 See Ordering Information for stability options.
- 2. Phase Noise performance may vary based on output frequency. See example plot at 10MHz below.



MODEL 580 STRATUM 3 TCXO/VC-TCXO - CLIPPED SINE

MECHANICAL SPECIFICATIONS

PACKAGE DRAWING



D.U.T. PIN ASSIGNMENTS

PIN	SYMBOL	DESCRIPTION							
1	V _C	Control Voltage – VCTCXO							
	v C	NC - TCXO							
2	GND	Circuit & Package Ground							
3	Output	Clipped Sine Wave Output							
4	V_{cc}	Supply Voltage							

1. DC-Cut Capacitor Required. Add 1000pF capacitor between TCXO output and input of load.

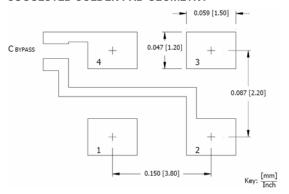
- MARKING INFORMATION 1. M580 - CTS Model Series.
- 2. − Pin 1 identifier.
- 3. C CTS identifier.4.
- 4. D Date code. See Table II for codes.
- 5. xxx Frequency Code.

Refer to document 016-1454-0, Frequency Code Tables.

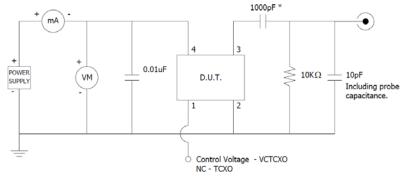
NOTES

- 1. DO NOT make connections to non-labeled pins. Castellation pins may have internal connections used in the manufacturing process.
- 2. Termination pads (e4); barrier plating is nickel [Ni] with gold [Au] flash plate.
- Reflow conditions per JEDEC J-STD-020, 260°C maximum.
- 4. MSL = 1.

SUGGESTED SOLDER PAD GEOMETRY



TEST CIRCUIT - CLIPPED SINE LOAD



^{*} DC-Cut Capacitor: Add 1000pF capacitor between the TCXO output and input of load.

TABLE II - DATE CODE

	MONTH			JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	ост	NOV	DEC	
	YEAR			JAN	TEB	WAK	AFK	IVIA	JON	JOL	AUG	SLI	001	NOV	DEC	
2001	2005	2009	2013	2017	Α	В	С	D	E	F	G	Н	J	K	L	М
2002	2006	2010	2014	2018	N	Р	Q	R	S	Т	U	V	W	Х	Υ	Z
2003	2007	2011	2015	2019	а	b	С	d	е	f	g	h	j	k	I	m
2004	2008	2012	2016	2020	n	р	q	r	S	t	u	٧	W	Х	У	Z