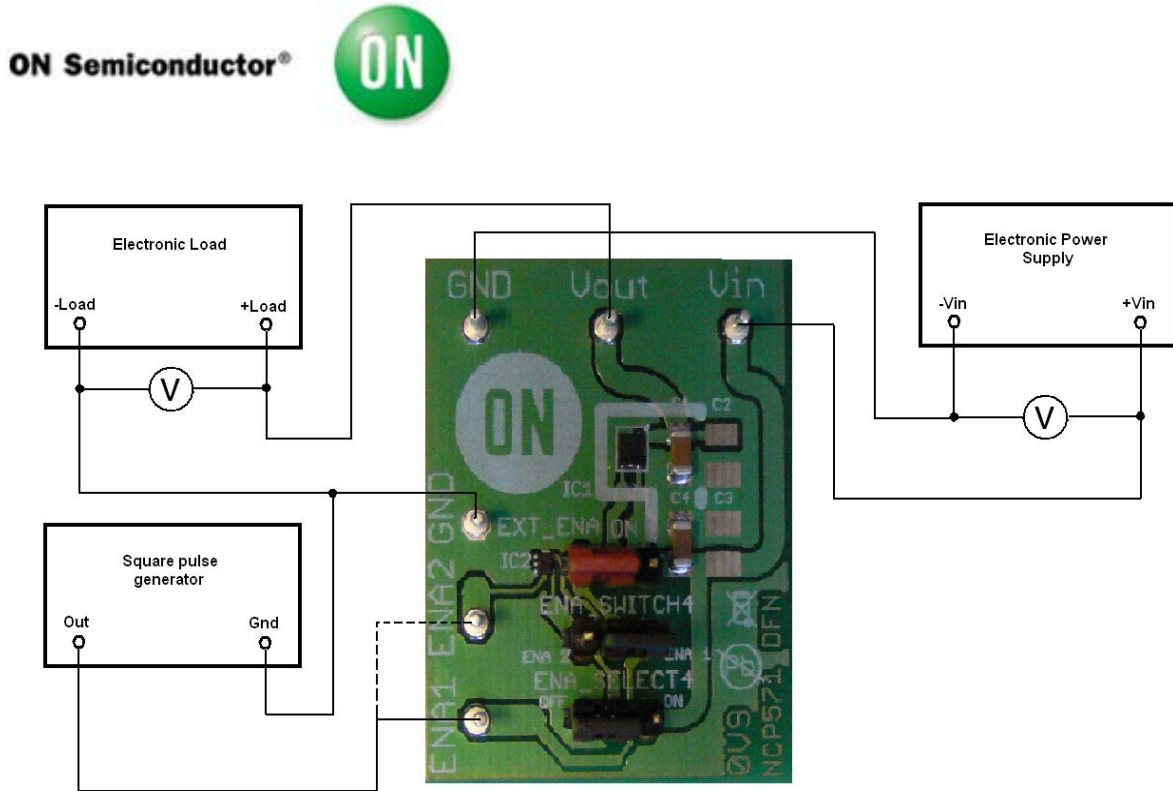


## Test Procedure for NCP571 0V9 DFN6 LDO Demoboard



### 1. Enable pin connected to Vin

1. Check the position of jumper and correct it if necessary.

- a) **EXT\_ENA** - ON
- b) **ENA\_SELECT** - ENA1 or ENA2
- c) **POWER ENA2** - OFF

2. Connect the test setup as shown Figure 1

3. Apply an input voltage **Vin = 2.7 V**

4. Apply **Iout = 0mA** load.

5. Check that **Vout** is **0.9 V**.

6. Increase **Iout** up to **150 mA**

7. Increase **Vin** up to **12 V** and decrease the load in accordance with **SOA**

8. Power down the Load

9. Power down the **Vcc**

10. End of test

## 2. Enable pin connected to pin ENA1

1. Check the position of jumper and correct it if necessary.
  - a) **EXT\_ENA** - **EXT\_ENA**
  - b) **ENA\_SELECT** - **ENA1**
  - c) **POWER ENA2** - **OFF**
2. Connect the test setup as shown Figure 1
3. Apply an input voltage **V<sub>in</sub> = 2.7 V**
4. Apply I<sub>out</sub> = 0mA load.
5. Check that V<sub>out</sub> is **0.9 V**.
6. Increase I<sub>out</sub> up to 150 mA
7. Increase V<sub>in</sub> up to 12 V and decrease the load in accordance with SOA
8. Apply the square pulse with **High level below V<sub>in</sub>** to pin ENA1
9. Check the output voltage and supply current.
10. Power down the Load.
11. Power down the V<sub>cc</sub>.
12. End of test.

## 3. Enable pin connected to pin ENA2

1. Check the position of jumper and correct it if necessary.
  - a) **EXT\_ENA** - **EXT\_ENA**
  - b) **ENA\_SELECT** - **ENA2**
  - c) **POWER ENA2** - **ON**
2. Connect the test setup as shown Figure 1
3. Apply an input voltage **V<sub>in</sub> = 2.7 V**
4. Apply I<sub>out</sub> = 0mA load.
5. Check that V<sub>out</sub> is **0.9 V**.
6. Increase I<sub>out</sub> up to **150 mA**
7. Increase V<sub>in</sub> up to **5.5 V** and decrease the load in accordance with SOA
8. Apply the square pulse to pin ENA2. The High level of ENABLE signal could be higher than input voltage up to **7 V**.
9. Check the output voltage and supply current.
10. Power down the Load.
11. Power down the V<sub>cc</sub>.
12. End of test.